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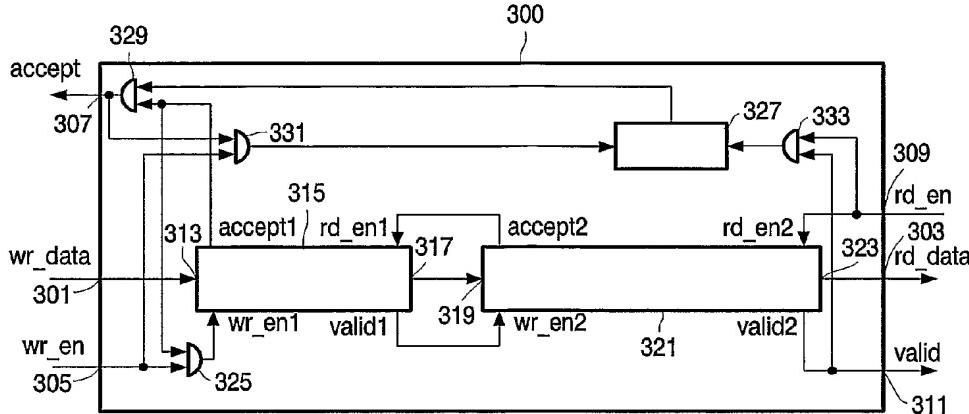
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(54) Title: A FIFO MEMORY DEVICE WITH NON-VOLATILE STORAGE STAGE



(57) Abstract: A FIFO memory device (300) comprises a storage device (321) which is a non-volatile FIFO comprising a plurality of non-volatile storage elements or latches. The FIFO memory device (300) also comprises an input stage (315) which is a volatile FIFO and comprises a plurality of volatile storage elements. The input stage (315) provides a temporary store for data and thus hides the latency of the storage device (321).

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